

AMENDMENTS TO THE CLAIMS

Claims 1-17, 24-30, 32-33, 40-46 were previously canceled . Please ADD claims 47-51 as follows:

1-17. (Canceled)

18. (Previously Presented) A method executed on hardware for a power reduction in a digital data interface communication data link, the method comprising:

- sending a link shut down packet from a host device to a client device;
- disabling a data driver to a high impedance state by the host device to place the digital data interface communication data link in hibernation;
- providing a logic one to a data line to drive the data line to a logic one state;
- toggling a strobe for a first predetermined period of time;
- driving the data line to a zero state and toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and
- transmitting a sub-frame header packet.

19. (Previously Presented) The method of claim 18 wherein disabling the data driver comprises producing a high impedance state to define a zero logic state while the communication data link is in hibernation.

20. (Previously Presented) The method of claim 18 wherein providing a logic one to the data line comprises providing the logic one by the host device.

21. (Previously Presented) The method of claim 18 wherein the data driver comprises a strobe driver.

22. (Previously Presented) The method of claim 18 wherein providing a logic one to the data line comprises providing the logic one by the client device causing the host device to drive the data line to the logic one.

23. (Previously Presented) The method of claim 18 further comprising conserving power during a time when data is not being transferred to and from the host device and client device.

24. (Canceled)

25. (Canceled)

26. (Canceled)

27. (Canceled)

28. (Canceled)

29. (Canceled)

30. (Canceled)

31. (Previously Presented) A storage media comprising program instructions which are hardware computer-executable to implement

a link shut down packet in a digital data interface communication data link to be sent from a host device to a client device, the storage media comprising;

program instructions that cause a data driver to be disabled to a high impedance state by the host device to place the digital data interface communication data link in hibernation;

program instructions that cause a logic one to be provided to a data line to drive the data line to a logic one state;

program instructions that cause a strobe to be toggled for a first predetermined period of time;

program instructions that cause the data line to be driven to a zero state and toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and

program instructions that cause a sub-frame header packet to be transmitted.

32. (Canceled)

33. (Canceled)

34. (Previously Presented) A hardware apparatus for power reduction in a digital data interface communication data link, the hardware apparatus comprising:

means for sending a link shut down packet from a host device to a client device;

means for disabling a data driver to a high impedance state by the host device to place the digital data interface communication data link in hibernation;

means for providing a logic one to a data line to drive the data line to a logic one state;

means for toggling a strobe for a first predetermined period of time;

means for driving the data line to a zero state and toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and

means for transmitting a sub-frame header packet.

35. (Previously Presented) The hardware apparatus of claim 34 wherein the means for disabling the data driver comprises means for producing a high impedance state to define a zero logic state while the communication data link is in hibernation.

36. (Previously Presented) The hardware apparatus of claim 34 wherein the means for providing a logic one to the data line comprises means for providing the logic one by the host device.

37. (Previously Presented) The hardware apparatus of claim 34 wherein the data driver comprises a strobe driver.

38. (Previously Presented) The hardware apparatus of claim 34 wherein means for providing a logic one to the data line comprises means for providing the logic one by the client device causing the host device to drive the data line to the logic one.

39. (Previously Presented) The hardware apparatus of claim 34 further comprises means for conserving power during a time when data is not being transferred to and from the host device and client device.

40. (Canceled)

- 41. (Canceled)
- 42. (Canceled)
- 43. (Canceled)
- 44. (Canceled)
- 45. (Canceled)
- 46. (Canceled)

47. (New) A hardware apparatus for power reduction in a digital data interface communication data link, the hardware apparatus comprising:
a host device configured to:
send a link shut down packet to a client device;
disable a data driver to a high impedance state to place the digital data interface communication data link in hibernation;
provide a logic one to a data line to drive the data line to a logic one state;
toggle a strobe for a first predetermined period of time;
drive the data line to a zero state and toggle the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and
transmit a sub-frame header packet.

48. (New) The hardware apparatus of claim 47 wherein the host is further configured to produce a high impedance state to define a zero logic state while the communication data link is in hibernation.

49. (New) The hardware apparatus of claim 47 wherein the data driver comprises a strobe driver.

50. (New) The hardware apparatus of claim 47 wherein the client device is configured to cause the host device to drive the data line to the logic one state.

51. (New) The hardware apparatus of claim 47 wherein the host device is further configured to conserve power during a time when data is not being transferred to and from the host device and client device.